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ID 2468711

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Device	Address	Length	Phase	Data	Description	Delta	Cmd.Phase.Offs (rep)	Driver
19.1	57 65 69 6e	4	OUT	74 65 6b 20	Weintek HMI	3.99ms		unknown
19.1	45 00 09 00	4	URB	78 44 a4 86	BULK/INT XFER	4us		1.2.16
19.1	54 d1 24 87	4	IRP	00 00 00 00		2us		1.3.0
19	06 00 d8 01	4	IRP	68 4a 93 86	c0 64 fb 84	2us		1.3.16
19	ae 4c 10 87	4	STAK	ae 4c 10 87	00 00 00 00	2us		1.4.0
19	02 00 00 e0	4	STAK	0a 22 db 84	00 00 00 00	2us		1.4.16
19.2	00 00 00 00	4	IN	30 60 2a 87	00 00 00 00	223us		2.1.0
19.2	21 32 33 34	4	IN	35 36 37 38	39 30	223us		2.1.0
19.2	48 00 09 00	4	URB	78 44 a4 86	22 00 00 00	5us		2.2.0
19	34 d1 24 87	4	IRP	00 00 00 00	e0 e4 0c 28	2us		2.2.16
19	06 00 d8 01	4	IRP	00 00 00 00	00 00 00 00	2us		2.3.0
19	28 7e ed 85	4	STAK	28 7e ed 85	00 00 00 00	2us		2.3.16
19	0f 00 00 e0	4	STAK	ae 0f f0 84	00 00 00 00	2us		2.4.0
19.1	00 00 00 00	4	OUT	30 60 2a 87	00 00 00 00	226us		2.4.16
19.1	48 00 09 00	4	URB	78 44 a4 86	20 00 00 00	4us		3.2.0
19.1	54 d1 24 87	4	IRP	00 00 00 00	00 00 00 00	4us		3.2.16
19	06 00 d8 01	4	IRP	68 4a 93 86	c0 64 fb 84	1us		3.3.0
19	ae 4c 10 87	4	STAK	ae 4c 10 87	00 00 00 00	2us		3.3.16
19	02 00 00 e0	4	STAK	0a 22 db 84	00 00 00 00	2us		3.4.0
19.2	00 00 00 00	4	IN	30 60 2a 87	00 00 00 00	223us		3.4.16
19.2	40 01 00 01	4	IN	40 46 32 37	41 31 32	6.8ms		4.1.0
19.2	28 42 28 39	4	URB	30 31 37 45	46 39 42 32	37 38 41 42		4.1.16
19.2	48 00 09 00	4	URB	78 44 a4 86	20 00 00 00	4us		4.2.0
19	34 d1 24 87	4	IRP	00 00 00 00	00 00 00 00	4us		4.2.16
19	06 00 d8 01	4	IRP	00 00 00 00	00 00 00 00	2us		4.3.0
19	28 7e ed 85	4	STAK	28 7e ed 85	00 00 00 00	2us		4.3.16
19	0f 00 00 e0	4	STAK	ae 0f f0 84	00 00 00 00	2us		4.4.0
19.1	00 00 00 00	4	OUT	30 60 2a 87	00 00 00 00	223us		4.4.16
19.1	40 01 00 02	4	OUT	40 41 34	44 34 39 20	25 34 20 46		5.1.0
19.1	46 42 39 34	4	URB	39 34 38 35	41 38 31 27	23 33 41 38		5.1.16
19.1	48 00 09 00	4	URB	78 44 a4 86	20 00 00 00	3us		5.2.0
19	34 d1 24 87	4	IRP	00 00 00 00	46 00 00 00	00 00 00 00		5.2.16
19	06 00 d8 01	4	IRP	68 4a 93 86	30 00 06 00	c0 64 fb 84		5.3.0
19	ae 4c 10 87	4	STAK	ae 4c 10 87	00 00 00 00	2us		5.3.16
19	0f 00 00 e0	4	STAK	94 1c 0d 85	00 00 00 00	2us		5.4.0
19.2	00 00 00 00	4	IN	30 60 2a 87	00 00 00 00	223us		5.4.16
19.2	40 01 00 03	4	IN	40 46 32 37	41 31 32	21ms		6.1.0
19.2	48 00 09 00	4	URB	78 44 a4 86	20 00 00 00	4us		6.2.0
19	34 d1 24 87	4	IRP	00 00 00 00	06 00 00 00	4us		6.2.16
19	06 00 d8 01	4	IRP	00 00 00 00	00 00 00 00	2us		6.3.0
19	28 7e ed 85	4	STAK	28 7e ed 85	00 00 00 00	2us		6.3.16
19	02 00 00 e0	4	STAK	ae 0f f0 84	00 00 00 00	2us		6.4.0
19.1	00 00 00 00	4	OUT	30 60 2a 87	00 00 00 00	223us		6.4.16
19.1	40 01 00 04	4	OUT	40 32 00 00	32 38 45 45	39 42 32 31		7.1.0
19.1	27 42 25 44	4	URB	34 36 37 34	38 34 34 42	22 39 43 38		7.1.16
19.1	48 00 09 00	4	URB	78 44 a4 86	20 00 00 00	4us		7.2.0
19	34 d1 24 87	4	IRP	00 00 00 00	38 00 00 00	00 00 00 00		7.2.16
19	06 00 d8 01	4	IRP	78 b5 4c 87	30 00 06 00	c0 64 fb 84		7.3.0
19	ae 4c 10 87	4	STAK	ae 4c 10 87	00 00 00 00	2us		7.3.16
19	0f 00 00 e0	4	STAK	94 1c 0d 85	00 00 00 00	3us		7.4.0
19.2	00 00 00 00	4	IN	30 60 2a 87	00 00 00 00	223us		7.4.16
19.2	40 01 01 09	4	IN	40 00 00 00	78 44 a4 86	39ms		8.1.0
19.2	48 00 09 00	4	URB	78 44 a4 86	20 00 00 00	4us		8.2.0
19	34 d1 24 87	4	IRP	00 00 00 00	06 00 00 00	4us		8.2.16
19	06 00 d8 01	4	IRP	00 00 00 00	00 00 00 00	3us		8.3.0
19	28 7e ed 85	4	STAK	28 7e ed 85	00 00 00 00	2us		8.3.16
19	0f 00 00 e0	4	STAK	ae 0f f0 84	00 00 00 00	1us		8.4.0
19	00 00 00 00	4	OUT	30 60 2a 87	00 00 00 00	223us		8.4.16

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